FAST RADIX 2, 3, 4, AND 5 KERNELS FOR FAST FOURIER TRANSFORMATIONS ON COMPUTERS WITH OVERLAPPING MULTIPLY–ADD INSTRUCTIONS*

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Abstract. We present a new formulation of fast Fourier transformation (FFT) kernels for radix 2, 3, 4, and 5, which have a perfect balance of multiplies and adds. These kernels give higher performance on machines that have a single multiply–add (mult–add) instruction. We demonstrate the superiority of this new kernel on IBM and SGI workstations.

Key word. FFT kernels

AMS subject classifications. 65-04, 42-04

PII. S1064827595281940

1. Introduction. Until several years ago, additions were faster on most computers than multiplications. Many attempts were therefore made to formulate FFT kernels with the smallest number of multiplies. Today multiplications are as fast as adds and most machines can do a multiplication and addition simultaneously. Among machines that can do this are high-performance workstations from IBM, HP, DEC, and SGI, as well as vector-supercomputers from Cray, NEC, and Fujitsu. Some machines achieve this parallelism by a mult–add instruction that is assigned during compilation. In this paper, we will derive FFT kernels that can be formulated uniquely in terms of mult–adds. The numerical implementation shows that they give considerable gain in speed.

2. Conventional FFT kernels. An FFT kernel [1, 2] calculates the innermost part in a transformation, which has the form

\[ Z_{\text{out}}(i) = \sum_{j=0}^{P-1} Z_{\text{in}}(j) \Omega^j \omega^{ij} \]

for \( i = 0, \ldots, P-1 \). The radix of the kernel is given by the prime factor \( P \) which is 2, 3, 4, or 5 in this paper. Both \( \Omega \) and \( \omega \) are complex numbers of modulus one. \( \Omega \) is called the twiddling factor [2] and \( \omega \) is given by \( e^{i \frac{2\pi}{P}} \). Efficient evaluation techniques for (2.1) can be found in [3, 4]. For completeness they will be repeated here. The real part of the array \( Z_{\text{in}} \) is denoted by \( z_{\text{inr}} \), the imaginary part by \( z_{\text{ini}} \), and correspondingly for \( Z_{\text{out}} \). The real part and imaginary parts of the twiddling factor \( \Omega^j \) are denoted by \( cr_j \) and \( ci_j \), respectively.

Radix 2

\[
\begin{align*}
  r1 &= z_{\text{inr}}(0) \\
  s1 &= z_{\text{ini}}(0) \\
  r &= z_{\text{inr}}(1) \\
  s &= z_{\text{ini}}(1) \\
  r2 &= r * cr_1 - s * ci_1
\end{align*}
\]

\[
\begin{align*}
  s2 &= r * ci_1 + s * cr_1 \\
  z_{\text{outr}}(0) &= r2 + r1 \\
  z_{\text{outi}}(0) &= s2 + s1 \\
  z_{\text{outr}}(1) &= r1 - r2 \\
  z_{\text{outi}}(1) &= s1 - s21
\end{align*}
\]

Received by the editors February 21, 1995; accepted for publication (in revised form) February 22, 1996.

http://www.siam.org/journals/sisc/18-6/28194.html

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Radix 4
r1 = zinr(0)
s1 = zini(0)
r = zinr(1)
s = zini(1)
r2 = r * cr1 - s * ci1
s2 = r * ci1 + s * cr1
r = zinr(2)
s = zini(2)
r3 = r * cr2 - s * ci2
s3 = r * ci2 + s * cr2
r = zinr(3)
s = zini(3)
r = r1 + r3
s = r2 + r4
zoutr(0) = r + s
zoutr(2) = r - s
zoutr(3) = r + s
zoutr(1) = r - s
zoutr(4) = r + s

Radix 3
\[ bb = 0.5 \times \sqrt{3} \]
r1 = zinr(0)
s1 = zini(0)
r = zinr(1)
s = zini(1)
r2 = r * cr1 - s * ci1
s2 = r * ci1 + s * cr1
r = zinr(2)
s = zini(2)
r3 = r * cr2 - s * ci2
s3 = r * ci2 + s * cr2
r = r2 + r3
s = s2 + s3
zoutr(0) = r + r1
zoutr(2) = r - r3
zoutr(1) = r1 - s2
zoutr(4) = r1 + r2
zoutr(3) = r1 - r2

Radix 5
\[ \cos_2 = \cos(2 \times \pi / 5) \]
\[ \cos_4 = \cos(4 \times \pi / 5) \]
\[ \sin_2 = \sin(2 \times \pi / 5) \]
\[ \sin_4 = \sin(4 \times \pi / 5) \]
r1 = zinr(0)
s1 = zini(0)
r = zinr(1)
s = zini(1)
r2 = r * cr1 - s * ci1
s2 = r * ci1 + s * cr1
r = zinr(2)
s = zini(2)
r3 = r * cr2 - s * ci2
s3 = r * ci2 + s * cr2
r = r2 + r3
s = s2 + s3
zoutr(0) = r + r1
zoutr(2) = r5 + r25 + r34
zoutr(1) = r - s
zoutr(4) = r + s
zoutr(3) = r + s
r5 = r * cr4 - s * ci4
s5 = r * ci4 + s * cr4
r25 = r2 + r5
r34 = r3 + r4
s25 = s2 - s5
s4 = s3 - s4
r = cos2 * r25 + cos4 * r34 + r1
s = sin2 * s25 + sin4 * s34
zoutr(1) = r - s
zoutr(4) = r + s
zoutr(3) = r + s
r25 = r2 - r5
zouti(0) = s1 + s25 + s34  
\[ r = \cos_4 * s25 + \cos_2 * s34 + s1 \]
\[ s = \sin_2 * r25 + \sin_4 * r34 \]
zouti(1) = r + s  
zouti(4) = r - s

3. The new FFT kernels. The first step in all of the above conventional kernels is the multiplication of the input elements \( Z_{in} \) with the twiddling factor. In this part there is an imbalance of multiplies and adds. For both the real part and imaginary part one needs two multiplies but only one add. The add capacities are thus sitting idle, resulting in a reduction of performance. In the following, we will show that by redefining the twiddling factors one can formulate these kernels in such a way that one has a perfect balance of multiplies and adds. In the part coming from the multiplication with the twiddling factor, we get rid of one multiply, giving higher performance. In the remaining part, we have to introduce some additional multiplies but only at places where up to now we had an unbalanced add. Even though we have more operations in this part, the CPU time does not increase since they can be done simultaneously on the machines we are considering.

Radix 2
\[ s2 = r * ci_1 + s \]
\[ r1 = zinr(0) \]
\[ s1 = zini(0) \]
r = zinr(1)  
s = zini(1)  
r2 = r - s * ci_1

Radix 4
\[ s4 = r * ci_3 + s \]
\[ r = r1 + r3 * cr_2 \]
\[ s = r2 + r4 * cr_31 \]
\[ zoutr(0) = r + s * cr_1 \]
zouti(0) = s2 * cr_1 + s1  
zoutr(1) = - r2 * cr_1 + r1  
zouti(1) = - s2 * cr_1 + s1

Radix 3
\[ ci_2 = ci_2 / cr_2 \]
\[ cr_21 = cr_2 / cr_1 \]
\[ bb = .5 * \sqrt{3} \]
\[ crbb_1 = cr_1 * bb \]
\[ ci_1 = ci_1 / cr_1 \]
\[ crh_1 = .5 * cr_1 \]
\[ bb = .5 * \sqrt{3} \]
\[ crbb_1 = cr_1 * bb \]
\[ ci_1 = ci_1 / cr_1 \]
\[ r = zinr(1) \]

zoutr(0) = r2 * cr_1 + r1  
zouti(0) = s2 * cr_1 + s1  
zoutr(1) = - r2 * cr_1 + r1  
zouti(1) = - s2 * cr_1 + s1
two additional ones are needed. In all cases, however, the kernels can be implemented the same in the two sets of kernels, with the exception of the radix 5 kernel where the total number of coefficients related to the trigonometric quantities is, however, multiple or many dimensional FFTs, this additional overhead is completely negligible. That are easily calculated such as the ratio of two twiddling cosines. In case one does tangent ( = sine/cosine). For some radices, in addition we need some other quantities that are easily calculated such as the ratio of two twiddling cosines.

\[
s = \text{zini}(1) \\
r_2 = r - s \times c_1 \\
s_2 = r \times c_1 + s \\
r = \text{zinr}(2) \\
s = \text{zini}(2) \\
r_3 = r - s \times c_2 \\
s_3 = r \times c_2 + s \\
r = r_2 + r_3 \times c_{21} \\
s = s_2 + s_3 \times c_{21} \\
\text{zoutr}(0) = r \times c_1 + r_1
\]

**Radix 5**

\[
\cos_2 = \cos(2 \times \pi / 5) \\
\cos_4 = \cos(4 \times \pi / 5) \\
\sin_2 = \sin(2 \times \pi / 5) \\
\sin_4 = \sin(4 \times \pi / 5) \\
c_1 = c_{11}/c_1 \\
c_2 = c_{12}/c_2 \\
c_3 = c_{13}/c_3 \\
cr_{23} = c_{23}/c_3 \\
cos_{24} = \cos_2 \times cr_{3} \\
cos_{44} = \cos_4 \times cr_{3} \\
c_4 = c_{41}/c_4 \\
cr_{14} = c_{11}/c_4 \\
cos_{25} = \cos_2 \times cr_{4} \\
sin_{25} = \sin_{2} \times cr_{4} \\
cos_{45} = \cos_4 \times cr_{4} \\
sin_{45} = \sin_4 \times cr_{4} \\
sin_{24} = (\sin_2/\sin_4) \times (cr_3/cr_4) \\
sin_{44} = (\sin_4/\sin_2) \times (cr_3/cr_4) \\
r_1 = \text{zinr}(1) \\
s_1 = \text{zini}(1) \\
r = \text{zinr}(2) \\
s = \text{zini}(2) \\
r_2 = r - s \times c_1 \\
s_2 = r \times c_1 + s \\
r = \text{zinr}(3) \\
s = \text{zini}(3) \\
r_3 = r - s \times c_2 \\
s_3 = r \times c_2 + s \\
r = \text{zinr}(4) \\
s = \text{zini}(4) \\
r_4 = r - s \times c_3 \\
\]

As we see, instead of the sine and cosine of the twiddling angle we need its sine and tangent ( = sine/cosine). For some radices, in addition we need some other quantities that are easily calculated such as the ratio of two twiddling cosines. In case one does multiple or many dimensional FFTs, this additional overhead is completely negligible. The total number of coefficients related to the trigonometric quantities is, however, the same in the two sets of kernels, with the exception of the radix 5 kernel where two additional ones are needed. In all cases, however, the kernels can be implemented
Table 1
Number of CPU cycles needed for one pass through a radix P kernel on a machine with multiply–add.

<table>
<thead>
<tr>
<th>Radix</th>
<th>Conventional kernel</th>
<th>New kernel</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>8</td>
<td>6</td>
</tr>
<tr>
<td>4</td>
<td>28</td>
<td>22</td>
</tr>
<tr>
<td>3</td>
<td>20</td>
<td>16</td>
</tr>
<tr>
<td>5</td>
<td>48</td>
<td>40</td>
</tr>
</tbody>
</table>

Table 2
Comparison of required operations for conventional and new kernels on a machine without mult–add instruction.

<table>
<thead>
<tr>
<th></th>
<th>conventional kernel</th>
<th>new kernel</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>mults</td>
<td>adds</td>
</tr>
<tr>
<td>Radix 2</td>
<td>4</td>
<td>6</td>
</tr>
<tr>
<td>Radix 4</td>
<td>12</td>
<td>22</td>
</tr>
<tr>
<td>Radix 3</td>
<td>12</td>
<td>16</td>
</tr>
<tr>
<td>Radix 5</td>
<td>32</td>
<td>40</td>
</tr>
</tbody>
</table>

with 32 floating point registers available on practically all RISC processors. Neglecting cycles that are needed to load or store data, the number of cycles is reduced as shown in Table 1.

Whereas in the framework of conventional kernels a radix 8 kernel does give some additional savings compared with radix 4, this is not true for these new types of kernels. Within this framework it would also not make sense to use number-theoretic prime factor FFTs, since the main motivation of these techniques is to get rid of the twiddling factor. Note also that by precalculating products in the new kernels, one can nearly come back to the operation counts of conventional kernels as shown in Table 2. In the unlikely case that one should use a machine that does not have a multiply–add facility, one does not lose very much.

4. Accuracy of the new kernels. The new kernels consist of repeated transformations of the type

\[ ax + by \rightarrow a(x + (b/a)y). \]

Instead of adding/subtracting \( ax \) and \( by \), one adds/subtracts the scaled quantities \( x \) and \( (b/a)y \). Since both operands are scaled in the same way the error accumulation properties of the new kernels and old kernels are the same. The slow increase of the error propagation has been verified for all the kernels, and in Figs. 1 and 2 the error propagation properties are shown for the case of radix 4 kernels. The fact that the error propagation of the new set of kernels is slightly better comes from the fact that no intermediate rounding is performed in a mult–add instruction. The accuracy of a mult–add instruction therefore exceeds the IEEE standard.

5. Timing results. The two sets of kernels were compared on an IBM RS6000/590 and an SGI/MIPS R8000 workstation; both have a single mult–add instruction. Both also have two floating point units such that two mult–add instructions can be done within one cycle. In Table 3, the timing results of multiple FFTs are shown for data sets that fit into cache. For the tests of radix 4, 3, and 5 transforms of length 64, 81, and 125 were used, respectively. If the cache is large enough, one can do a large number of multiple FFTs and loop overheads become less important.
The relative error of a sequence of forward-backward transforms of data sets containing up to one million items. The solid line is obtained with the new kernel, the dashed line with the conventional kernel of the ESSL library. As can be seen the error growth is very similar for both. The tests were done with eight bytes floating point numbers.

The errors in the Fourier spectrum of a sequence that is a superposition of three frequencies and where the exact result is therefore known. The solid line gives the relative error for the nonzero weight frequencies, the dashed line the absolute error for the zero weight frequencies. The results obtained with the new kernel and the ESSL library are indistinguishable on the scale of the figure.

Since the cache is much larger on the SGI machine, 1024 multiple FFTs could be done, whereas on the IBM machine only 64 could be fit into cache. Even for many simultaneous FFTs it is not possible to reach the theoretical limit. On the SGI machine, for instance, it is not possible to overlap the branching decision adding at least
Table 3

Measured cycles for radix 3, 4, and 5 with different combinations of kernels and machines. In the header, the number in brackets gives the number of simultaneous FFTs; in the remaining lines the numbers in brackets give the theoretical limit assuming that the code would run at the peak speed attainable by the machine.

<table>
<thead>
<tr>
<th>Radix</th>
<th>IBM ESSL (64)</th>
<th>IBM new kernel (64)</th>
<th>SGI new k. (64)</th>
<th>SGI new k. (1024)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>14.9 (14)</td>
<td>12.3 (11)</td>
<td>14.1 (11)</td>
<td>13.2 (11)</td>
</tr>
<tr>
<td>3</td>
<td>11.3 (8)</td>
<td>11.1 (8)</td>
<td>10.2 (8)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>27.0 (20)</td>
<td>62.0 (20)</td>
<td>62.0 (20)</td>
<td></td>
</tr>
</tbody>
</table>

one cycle per loop iteration. It is also not always possible to overlap all the address calculations. The comparison with the ESSL library (version 2.2.1) could be done only for powers of 4, since it does not allow arbitrary powers of 3 and 5. The radix 2 kernel is load/store bound on the SGI machine and was therefore not included in the test. For radix 5 the SGI compiler was not able to generate an efficient machine code, resulting in very poor performance.

6. Conclusions. To obtain very high performance on modern computer architectures, it is very helpful to have an alternating stream of multiply and add instructions. We have presented FFT kernels that give rise to such an instruction pattern. Timing results show that these kernels clearly outperform other kernels. A very good compiler is a necessity to obtain an optimal instruction pattern with a FORTRAN program.

Acknowledgments. We thank the Cornell Theory Center in Ithaca, NY, and Dr. Rappe at the University of Pennsylvania for giving us access to their machines.

REFERENCES